



Paper code : ELL:205
Paper Name : Electronic Lab II (Digital Electronics)

List of Experiments:

1. Verification of Basic logic gates
2. Verification of EX-OR and EX-NOR gate
3. Verification of Universal logic gates.
4. Verification of De-Morgan's 1st law.
5. Verification of De-Morgan's 2nd law.
6. Universal building block using NAND gate
7. Universal building block using NOR gate
8. Study of Half Adder
9. Study of Full Adder.
10. Study of Half Sub tractor
11. Study of Full Sub tractor.
12. 4-1 Multiplexer
13. 8-1 Multiplexer
14. 1-4 De-multiplexer
15. 1-8 De-multiplexer
16. Encoders
17. Decoders
18. 4-bit counters
19. Decade counters
20. Down counters